

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

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1. (Currently Amended) An intensified solid-state imaging sensor, comprising:
 - a. a photo cathode having an input side for receiving light from an image, and an output side from which electrons produced by the photo cathode exit;
 - b. a microchannel plate having an input surface positioned adjacent to the output surface of the photo cathode, an output surface from which an increased number of electrons exit, and a plurality of channels formed between the input surface and the output surface;
 - c. a first electrical connection for applying a first biasing voltage between the photo cathode and the microchannel plate;
 - d. a solid-state imaging device having an electron receiving surface, positioned adjacent to the output surface of the microchannel plate, for receiving the increased number of electrons output from the microchannel plate, and an output for outputting an intensified image signal;
 - e. a second electrical connection for applying a second biasing voltage between the microchannel plate and the solid-state imaging sensor; and
 - f. a vacuum body holding the photo cathode, microchannel plate and solid-state imaging device together as a unit; and
 - g. an insulating layer separating the output surface of the microchannel plate and the electron receiving surface of the solid-state imaging sensor.
2. (Original) An intensified solid-state imaging sensor according to claim 1, wherein the first biasing voltage is no more than about 2000V.
3. (Original) An intensified solid-state imaging sensor according to claim 1, wherein the second biasing voltage is no less than approximately 100V.

4. (Original) An intensified solid-state imaging sensor according to claim 1, wherein the solid-state imaging device is a CCD.

5. (Original) An intensified solid-state imaging sensor according to claim 4, wherein the CCD is a back thinned charge-coupled device.

6. (Original) An intensified solid-state imaging sensor according to claim 4, wherein the CCD is a standard charge coupled device.

7. (Original) An intensified solid-state imaging sensor according to claim 1, wherein the solid-state imaging device is a CMOS imaging sensor.

8. (Original) An intensified solid-state imaging sensor according to claim 1, wherein the output surface of the microchannel plate and the electron receiving surface of the solid-state imaging sensor are at least in part in physical contact.

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10. (Currently Amended) An intensified solid-state imaging sensor according to claim 91, wherein the insulting layer comprises a thin film deposited on one of either the electron receiving surface of the solid-state imaging device, or the output surface of the microchannel plate.

11. (Original) An intensified solid-state imaging sensor according to claim 10, wherein the thin film is no more than approximately 10 μm thick.

12. (Original) An intensified solid-state imaging sensor according to claim 1, wherein each of the plurality of channels has a substantially circular cross section.

13. (Original) An intensified solid-state imaging sensor according to claim 1, wherein each of the plurality of channels has a substantially square cross section.

14. (Original) An intensified solid-state imaging sensor according to claim 1, wherein each of the plurality of channels in the microchannel plate has a predetermined width, and are separated by a predetermined pitch.

15. (Original) An intensified solid-state imaging sensor according to claim 14, wherein the solid-state imaging sensor comprises a plurality of collection wells of a predetermined size.

16. (Original) An intensified solid-state imaging sensor according to claim 15, wherein each of the plurality of channels is positioned relative to one of the plurality of collection wells so that electrons are emitted from the plurality of channels toward a predetermined number of collection wells.

17. (Original) An intensified solid-state imaging sensor according to claim 16, wherein the predetermined pitch, channel width, and collection well size are such that electrons emitted from two or more channels are received by a single collection well.

18. (Original) An intensified solid-state imaging sensor according to claim 16, wherein the predetermined pitch, channel width, and collection well size are such that electrons from one channel are received by a single collection well.

19. (Original) An intensified solid-state imaging sensor according to claim 16, wherein the predetermined pitch, channel width, and collection well size are such that electrons from one channel are received by a two or more collection wells.

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Ans 20. (Currently Amended) An intensified solid-state imaging sensor, comprising:

- a. a photo cathode;
- b. a microchannel plate;
- c. a solid-state imaging sensor that receives electrons output from the microchannel plate and outputs an intensified image signal;
- d. an electric biasing circuit connected between the photo cathode, microchannel plate, and solid-state sensor; and
- e. a vacuum body holding the photo cathode, microchannel plate, and solid-state imaging sensor in close physical proximity to one another; and

f. an insulating layer separating an output surface of the microchannel plate and an electron receiving surface of the solid-state imaging sensor.

21. (Currently Amended) An intensified solid-state imaging sensor, comprising;

- a. a photo cathode to convert light from an image into electrons;
- b. an electron multiplying device connected to an output surface of the photo cathode, the electron multiplying device being adapted to receive electrons from the photo cathode, increase the number of electrons received, and output the increased number of electrons;
- c. a solid-state image sensor, further comprising:
 - i. a plurality of collection wells connected to the electron multiplying device to receive the increased number of electrons output from the electron multiplying device and generate an intensified image signal; and
 - ii. an output for outputting the intensified image signal to a display device;
- d. an electrical circuit connected between the photo cathode and the solid-state image sensor to apply at least one biasing voltage there between; and
- e. a body in which the photo cathode, electron-multiplying device, and solid-state image sensor are held in at least a partial vacuum; and
- f. an insulating layer separating an output surface of the electron multiplying device and an electron receiving surface of the solid-state image sensor.

22. (Newly Added) A CMOS imager comprising:

- a. a photo cathode having an input side for receiving light from an image, and an output side from which electrons produced by the photo cathode exit;
- b. a microchannel plate having an input surface positioned adjacent to the output surface of the photo cathode, an output surface from which an increased number of

electrons exit, and a plurality of channels formed between the input surface and the output surface;

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- c. a CMOS sensor having an electron receiving surface, positioned adjacent to the output surface of the microchannel plate, for receiving the increased number of electrons output from the microchannel plate, and an output for outputting an intensified image signal; and
- d. a vacuum body holding the photo cathode, microchannel plate and CMOS sensor together as a unit.